

PATENT ABSTRACTS OF JAPAN

(11)Publication number : 02-302173
(43)Date of publication of application : 14.12.1990

(51)Int. CI. H04N 1/393
// B41J 2/485
G03G 15/04

(21)Application number : 01-121620 (71)Applicant : KYOCERA CORP
(22)Date of filing : 17.05.1989 (72)Inventor : NAGATA KATSUMI

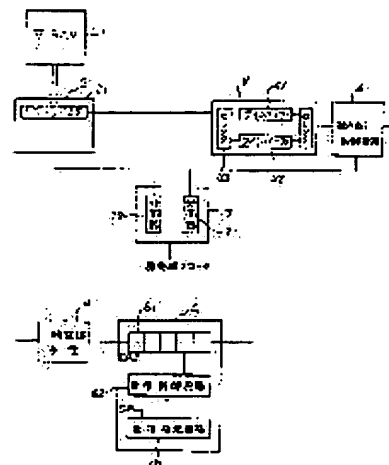
(54) PICTURE PROCESSING SYSTEM

(57)Abstract:

PURPOSE: To obtain a magnified picture corresponding to a magnified size by using a readout clock with a frequency different from a frequency of a write clock so as to read out a picture data written in a storage means through the use of a prescribed write clock while applying repetitive processing.

CONSTITUTION: A readout control circuit 6 reads out a picture data after repetitive processing via a selector 43 from line buffers 41, 42 as it is or while applying repetitive processing as required. A clock generating circuit. 7 consists of a couple of frequency dividers 71, 72 generating write and readout clocks. The frequency divider 72 generates a write clock synchronously with a picture input clock by applying 1/4 frequency division to the original oscillation clock, while the frequency divider 71 applies 1/3 frequency division to the original oscillation clock to generate the readout clock having a clock period with 75% of the write clock.

BEST AVAILABLE COPY



LEGAL STATUS

[Date of request for examination]

[Date of sending the examiner's decision of rejection]

[Kind of final disposal of application other than the examiner's decision of rejection or application converted registration]

[Date of final disposal for application]

[Patent number]

[Date of registration]

[Number of appeal against examiner's decision of rejection]

[Date of requesting appeal against examiner's decision of rejection]

[Date of extinction of right]

Copyright (C); 1998, 2003 Japan Patent Office